An Efficient Turbo Equalization for Faster than Nyquist Signal

Chang-Uk Baek and Ji-Won Jung
Radio Communication Engineering, Korea Maritime and Ocean University, Busan, Korea
Email: {cubaek, jwjung}@kmou.ac.kr

Abstract—In this paper, we analyzed efficient decoding scheme with faster than Nyquist (FTN) signaling that is transmission method faster than Nyquist theory and increase the throughput. We proposed BCJR equalization model to minimize inter symbol interference when faster than Nyquist signal is transmitted. The presented model utilized interference as branch information and iteratively exchange probabilistic information between BCJR and LDPC decoder. In BCJR equalizer, the performance depends on Euclidean distance of branch metrics between possible transitions at each node, in order to maximize Euclidean distances, we proposed FTN re-mapper by reordering the branch matrices on trellis diagram. We confirmed that performance was improved compared to conventional methods as increasing throughput of faster than Nyquist signal.

Index Terms—faster than Nyquist, BCJR equalizer, LDPC codes

I. INTRODUCTION

Faster-Than-Nyquist (FTN) signalling is a technique of transmitting information at a rate higher than the allowed Nyquist limit [1]-[4]. Systems employing this technique have shown to achieve higher information rates at the cost of increased processing in the transmitter and the receiver. There have been some efforts to apply FTN theory to commercial applications, e.g. DVB-S2 for the satellite broadcasting system. It evaluated the performance of DVB-S2 building block based on FTN transmission, and this result gives an opportunity for the practical implementation. There is increasingly growing demand to send high data rates over satellite channels. We here discuss utilizing time packing or FTN signalling to satisfy this demand in combination with using tight frequency spacing is 

\[ s(t) = E \sum \alpha_n h(t - nT) \]  

Here \( \alpha_n \) is a sequence of independent M-ary data symbols, each with energy \( E \), and a new unit-energy pulse are \( h(t) \) appears each \( T \) seconds, the symbol time. The factor \( \tau \) can be thought of as a time acceleration factor since now the pulses come too fast by a factor \( 1/\tau \).

If a filter matched to \( h(t) \) is used in the detection, its samples are no longer the M-ary values +1, -1, +1, -1… that add up to the heavier curve \( s(t) \).

II. FTN SIGNAL MODELING

With this background, we turn to FTN signalling. The key aspect of the FTN method is that \( h(t) \) is no longer orthogonal with respect to the symbol time. The same \( h \) is employed but the symbol time is \( \tau T, \tau < 1 \). The signal becomes:

\[ s(t) = E \sum \alpha_n h(t - nT) \]  

Above is ordinary orthogonal linear modulation with \( h(t) = (\sqrt{1/\tau}) \sin(\pi t/T) \), with the lighter sinc pulses representing symbols -1, +1, -1, +1… that add up to the heavier curve \( s(t) \).
Fig. 1 shows an example of FTN signal with orthogonal symbol time \( \tau = 1 \) and \( \tau = 0.8 \). Fig. 1(a) is ordinary signal with Nyquist rate, and Fig. 1(b) shows FTN signal with \( \tau = 0.8 \). It can be seen than five sinc pulses are now advanced.

III. AN EFFICIENT DECODER STRUCTURE FOR FTN MODEL

Fig. 2 displays FTN transmission system based on DVB-S2 standards. Let us suppose that the binary digits delivered by source are encoded by a LDPC encoder. The encoded data are reordered by an interleaver, and applied QPSK modulation point with FTN mapper. At the receiver side, an LLR computer is needed to convert the equalizer output, assumed to follow Gaussian distribution, into extrinsic LLRs regarding the code bits, by using a priori LLRs from the previous decoding iteration. This updated set of soft information about the code bits is then deinterleaved and provided as a priori LLRs, for the next decoding iteration.

The computation of the LLRs pertaining to the equalizer uses the FTN rate and roll-off values in reconstructing ISI in each carrier. Generally, the decoding method of trellis type as shown in Fig. 2 is BCJR algorithm [9], [10] with soft decision value. BCJR algorithm is a well-known maximum a posteriori probability decoding algorithm which has been proposed earlier for point to point communication applications.

The value of \( L_v^0 \) after interleaver is computed as \( L_v^0 = L_v' - L_v'' \), then input to the turbo decoder. The estimated extrinsic value of \( L_v^0 \) at decoder output is given by:

\[
L_v^0 = \log \frac{p(x = +1)}{p(x = -1)}
\]

The extrinsic value \( L_v^0 \) of which calculates the post probability is error correction terms. The re-interleaving of computed value as \( L_v^0 \) is input to BCJR equalizer, then \( L_v' \) is updated in order to compensate for the errors.

Fig. 3 shows block diagram of FTN mapper. As shown in Equation (1), take into account acceleration factor \( \tau \) and M interference signal at time k, quantities may be written in the form:

\[
\Gamma_{i}(t) = \sum_{n=0}^{M-1} a_{n} h(t - mtT)
\]

And let us suppose that ISI is limited (L1+L2) symbols. Equation (1) may be written in the form:

\[
R_{i} = \sum_{k=z_{i}}^{c_{i}+L} \Gamma_{k}(t) c_{k} + b_{i}
\]

Based on Fig. 4(a) and Table I, the Euclidean distance between branch metrics at each node is very small. In BCJR equalizer, the performance depends on Euclidean
distance of branch metrics between possible transitions at each node, in order to maximize Euclidean distances, we proposed FTN re-mapper by changing the trellis diagram. In \( b^{\text{FTN}} \), Euclidean distance is depended on \( y \) symbols. Therefore, FTN re-mapper reconstruct trellis diagram as shown in Fig. 4(b). To maximize Euclidean distance, we change

\[
\text{(a) FTN mapper} \quad \text{(b) FTN re-mapper}
\]

![Trellis diagram](image)

Figure 4. Trellis diagram

A set of triples (previous state, channel output, next state) uniquely defines a finite state machine on which the BCJR operates. As an illustration a trellis is shown in Fig. 3(c). It has 4 states \((s_0, s_1, s_2, s_3)\), and each state is given by a different 2-bit pattern. The states in vertical columns represent all possible states that the channel (or FSM) can take at a given time instant, while the labeled edges represent possible transitions. Neighboring columns thus represent consecutive time instants.

Given \( s' \) the previous state, \( s = (c_{j-m}, c_{j-m+1}, L, c_j, c_{j+1}, c_{j+m}) \) - the present state, \( u = (u_1, u_2, L, u_n) \) - the transmitted code word, and \( y = (y_1, y_2, L, y_n) \) - the received sequence, the log-likelihood ratio (LLR) (denoting the bit reliability) of \( u_j (j = 1, 2, ..., n) \), is calculated as:

\[
L(u_j) = \max \left[ a_{j-1}(s') + \gamma_j(s', s) + \beta_j(s) \right] s', s, u_j = 1
\]

\[
\text{(5)}
\]

The forward metric, \( a_j(s) = \log p(s = s, y'_j) \) is given by:

\[
a_j(s) = \max \left[ a_{j-1}(s') + \gamma_j(s', s) \right]
\]

\[
\text{(6)}
\]

The backward metric, \( \beta_j(s') = \log p(y'_{j+1} | s' = s) \) as:

\[
\beta_j(s') = \max \left[ \beta_j(s) + \gamma_j(s', s') \right]
\]

\[
\text{(7)}
\]

And the branch metric \( \gamma_j(s', s) \) is given by:

\[
\gamma_j(s', s) = \log p(s = s, y_j | s_j = s') = \log p(y_j | u_j) p(u_j)
\]

\[
\text{(8)}
\]

The \( \max' \) operator is defined as:

\[
\max' (x, y) = \max(x, y) + \log \left( 1 + e^{F-1} \right)
\]

LDPC decoder and BCJR equalizer are connected through the interleaving and de-interleaving that update each other's information repeatedly. The inner coded bits are then subtracted from the input and interleaved. The interleaved output is cancelled a posteriori from the proceeding received signal. Interleaving helps receiver convergence.

IV. SIMULATION RESULTS

Computer simulations are used to study the performance evaluation. For the comparison purpose, Table II listed parameters for simulation.

<table>
<thead>
<tr>
<th>TABLE II. TYPE SIZES FOR CAMERA-READY PAPERS</th>
</tr>
</thead>
<tbody>
<tr>
<td>Channel coding</td>
</tr>
<tr>
<td>Coding rate</td>
</tr>
<tr>
<td>( \tau )</td>
</tr>
<tr>
<td>Modulation</td>
</tr>
<tr>
<td>Inner iteration</td>
</tr>
<tr>
<td>Outer iteration</td>
</tr>
</tbody>
</table>

In the Table II, inner iteration means iteration number of LDPC decoder itself and outer iteration means total iteration number between LDPC decoder and BCJR equalizer.
In aspect to performance of proposed algorithm which using the FTN re-mapper in order to maximize the Euclidean distance, coding gain of 0.5dB can be achieved compared to conventional algorithm with using only FTN mapper.

REFERENCES


Chang Uk Baek is first author. He was born in June, 1984, Republic of Korea. He received a B.S degree in Radio Communication Engineering from Korea Maritime and Ocean University, Busan, Republic of Korea, in February 2012. Currently, he is working on a M.S degree in Dept. of Radio Communication Engineering, Korea Maritime and Ocean University, Busan, Republic of Korea. His research interests are channel coding, digital modem, FPGA design technology, digital broadcasting systems, and underwater acoustic communications.

Ji Won Jung is corresponding author. He was born in Busan, South Korea in 1966. He received his B.S., M.S., and Ph.D. degrees from Sungkyunkwan University, Seoul, Korea, in 1989, 1991, and 1995, respectively, all in electronics engineering. From November 1990 to February 1992, he was with the LG Research Center, Anyang, Korea. From September 1995 to August 1996, he was with Korea Telecom(KT). From August 2001 to July 2002, he was an Invited Researcher with the Communication Research Center Canada [supported by Natural Sciences and Engineering Research Council of Canada(NSERC)]. Since 1996, he has been with the Department of Radio Science and Engineering, Korea Maritime University, Busan, Korea. His research interests are channel coding, digital modem, FPGA design technology, digital broadcasting systems, and underwater acoustic communications.