

International Journal of Signal Processing Systems

CONTENTS

Volume 3, Number 2, December 2015

Pattern Recognition and Biological Signal Processing

Thermal Face Recognition Using Moments Invariants94
Naser Zaeri, Faris Baker, and Rabie Dib

Analysis of Brain Signals for the Discrimination of Observations of Correct and Incorrect Actions100
P. A. Asvestas, S. A. Kostopoulos, E. M. Ventouras, A. Korda, G. K. Matsopoulos, and I. S. Karanasiou

Classification of Asthmatic Breath Sounds by Using Wavelet Transforms and Neural Networks106
Fatma Z. Göğüş, Bekir Karlık, and Güneş Harman

Using the Predictive Method of Adaptive 4x4/8x8 Transformations to Reduce the Computation in Motion Estimation 112
K.-H. Chou and C.-P. Chung

Electromyography Low Pass Filtering Effects on the Classification of Hand Movements in Amputated Subjects 118
Manfredo Atzori and Henning Müller

Voice Morphing Based on Spectral Features and Prosodic Modification 123
A. Qavi, S. A. Khan, and K. Basir

Input Support System for Medical Records Created Using a Voice Memo Recorded by a Mobile Device128
K. Kurumizawa, H. Nishizaki, K. Nishizaki, and H. Ikegami

Computer Network and Communications

Analysis of OOK Upstream Signal Remodulation for Different Data Rates in WDM PON Network 134
Amit Kumar Garg and Vijay Janyani

Analysis of Multicast Routing Protocols Using Quality of Service 139
N. Sah, N. R. Prakash, and D. Bagai

QoS Analysis in Mobile Ad-Hoc Networks Using Bandwidth Utilization Technique 146
N. Sah, N. R. Prakash, and D. Bagai

Introducing Orthus a Dual-Headed Authentication Protoco 153
Dean Rogers

Electronic Communications and Embedded Systems

Effect of High Altitude Aeronautical Platforms with Cognitive Relay for Radar Performance 159
Ashagrie Getnet Flattie

Design and Implementation of Power Efficient Micro Pipelined GALS Based 2-D FFT Processor Core 166
Y. J. M. Shirur and V. S. Chakravarthi

Reducing Motion Blur by Adjusting Integration Time for Scanning Camera with TDI CMOS 172
Haeng-Pal Heo and Sung-Woong Ra

A New Hardware Implementation of Base 2 Logarithm for FPGA177
A. M. Mansour, A. M. El-Sawy, M. S. Aziz, and A. T. Sayed